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Applicant(s): J. G. Walacavage et al.

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Filed: September 28, 2001

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Title: METHOD OF EMULATING MACHINE TOOL BEHAVIOR FOR PROGRAMMABLE LOGIC  
CONTROLLER LOGICAL VERIFICATION SYSTEM

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Date July 22, 2004

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Commissioner for Patents  
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**STATUS LETTER**

Sir:

The above-identified patent application has been on file with the United States Patent and Trademark Office for more than one year and no office actions have been received. Attorney for Applicants request the status of this application including the name and telephone number of the Examiner.

Respectfully submitted,  
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